OIPE JAN 25 2005 PATENT & TRADEMARK OFFICE

Docket No.: GR00P1528 D

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.	: 10/685,065	Confirmation No:
Applicant	: Johann Winderl, et al.	
Filed	: October 14, 2003	
Title	: Method for Applying a Semiconductor Chip to a Carrier Element	
Docket No.	: GR00P1528 D	
Customer No.	: 24131	

STATUS LETTER

Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450

S i r :

It is respectfully requested that this Status Letter be returned to:

LERNER AND GREENBERG, P.A.
Post Office Box 2480
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Tel: (954) 925-1100
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with an indication as to when an action may be expected.

Respectfully submitted,

Alexia J. Vrahimis

Date: January 21, 2005

/av